



MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE

(UGC-AUTONOMOUS INSTITUTION)

Affiliated to JNTUA, Ananthapuramu & Approved by AICTE, New Delhi

NAAC Accredited with A+ Grade, NIRF India Rankings 2022 - Band: 251-300 (Engg.)

NBA Accredited - B.Tech. (CIVIL, CSE, CST, ECE, EEE, MECH), MBA & MCA

www.mits.ac.in



Examination Branch

M. Tech (VLSI & ES) – I Year II Semester (R24) Regular End Semester Examinations, August-2025
(For 2024 Admitted batch)

TIME TABLE

Academic Year: 2024– 25

Time: 10:00 AM to 01:00 PM

Duration: 3 hours

DATE / DAY	Course Name & Code
04.08.2025 (Monday)	CMOS Analog IC Design-24VESP103
06.08.2025 (Wednesday)	Embedded System Design-24VESP104
11.08.2025 (Monday)	Pattern Recognition and Machine Learning-24VESP407
13.08.2025 (Wednesday)	Physical Design Automation-24VESP412

Note: (i) Any clashes or omissions in the time table may please be informed to the Controller of Examinations Immediately.
(ii) Even if Government declares holiday on any of the above dates, the examination shall be conducted as usual.


CONTROLLER OF EXAMINATIONS

Date: 04.07.2025

Copy to:

1. ECE HOD
2. Transport Incharge
3. Notice Board (AS & Dept)
4. File (AS)


PRINCIPAL

Principal
Madanapalle Institute of
Technology & Science
MADANAPALLE

E Copy To: Vice Principal (Academics), Vice Principal -PG Vice Principal (Administration)

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